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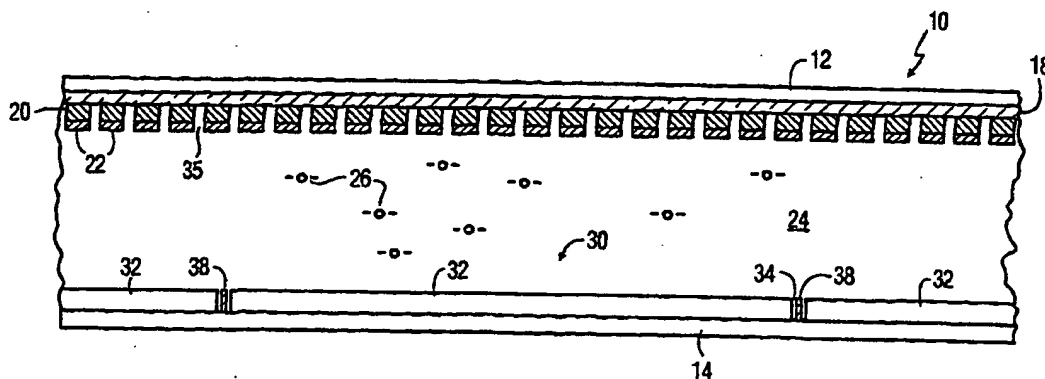
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(21) International Application Number: PCT/US94/09208 (22) International Filing Date: 15 August 1994 (15.08.94) (30) Priority Data: 118,515 9 September 1993 (09.09.93) US (71) Applicant: COPYTELE, INC. [US/US]; 900 Walt Whitman Road, Huntington Station, NY 11746 (US). (72) Inventors: LASPINA, Christopher, A.; 58 Cherry Lane, Syosset, NY 11791 (US). LEWITT, Edward, G.; 76 Dogleg Lane, Roslyn Heights, NY 11577 (US). (74) Agent: PLEVY, Arthur, L.; Plevy & Associates, P.O. Box 1366, 146 Route 1 North, Edison, NJ 08818-1366 (US).	(81) Designated States: CA, JP, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published <i>With international search report.</i> <i>With amended claims.</i>	

(54) Title: **ELECTROPHORETIC DISPLAY PANEL WITH SELECTIVE CHARACTER ADDRESSABILITY**

(57) Abstract

An electrophoretic display having a plurality of individual characters which are individually addressable for writing and erasing. The anode (32) of the display has a plurality of character blocks (32) which correspond to the character positions of the display. The character blocks (32) are interconnected by a series of character column (38) and row (36) control lines which are insulated from each other. Each character block (32) is connected to one row line (36) and one column line (38), respectively, by a resistor (40) and a diode (42) which are arranged in parallel. A further resistor (48) is connected in series with the diode (42). An individual character block is erased by applying a negative voltage to the specific column line and row line which cross at the desired character block.

ELECTROPHORETIC DISPLAY PANEL WITH SELECTIVE CHARACTER ADDRESSABILITY

BACKGROUND ART

This invention relates to electrophoretic display devices in general and, more particularly, to an electrophoretic display apparatus which employs means for selectively erasing and rewriting individual characters on the display panel thereof.

5 Electrophoretic displays (EPIDS) are now well known. A variety of display types and features are taught in several patents issued in the names of the inventors herein, Frank J. DiSanto and Denis A. Krusos and assigned to the assignee herein, Copytele, Inc. of Huntington Station, New York. For example, U.S. Patent Nos. 4,655,897 and 4,732,830, each entitled
10 **ELECTROPHORETIC DISPLAY PANELS AND ASSOCIATED METHODS** describe the basic operation and construction of an electrophoretic display. U.S. Patent No. 4,742,345, entitled **ELECTROPHORETIC DISPLAY PANELS AND METHODS THEREFOR**, describes a display having improved alignment and contrast. Many other patents regarding such displays are also assigned to
15 Copytele, Inc.

 The display panels shown in the above-mentioned patents operate upon the same basic principle, viz., if a suspension of electrically charged pigment particles in a dielectric fluid is subjected to an applied electrostatic field, the pigment particles will migrate through the fluid in response to the
20 electrostatic field. Given a substantially homogeneous suspension of particles having a pigment color different from that of the dielectric fluid, if the applied electrostatic field is localized it will cause a visually observable localized pigment particle migration. The localized pigment particle migration results either in a localized area of concentration or rarefaction of particles depending

upon the polarity and direction of the electrostatic field and the charge on the pigment particles.

The electrophoretic display apparatus taught in the foregoing U.S. Patents are "triode-type" displays having a plurality of independent, parallel, cathode row conductor elements or "lines" deposited in the horizontal
5 on one surface of a glass viewing screen. A layer of insulating photoresist material deposited over the cathode elements and photoetched down to the cathode elements to yield a plurality of insulator strips positioned at right angles to the cathode elements, forms the substrate for a plurality of
10 independent, parallel column or grid conductor elements or "lines" running in the vertical direction. A glass cap member forms a fluid-tight seal with the viewing window along the cap's peripheral edge for containing the fluid suspension and also acts as a substrate for an anode plate deposited on the interior flat surface of the cap. When the cap is in place, the anode surface is
15 in spaced parallel relation to both the cathode elements and the grid elements. Given a specific particulate suspension, the sign of the electrostatic charge which will attract and repel the pigment particles will be known. The cathode element voltage, the anode voltage, and the grid element voltage can then be ascertained such that when a particular voltage is applied to the cathode and
20 another voltage is applied to the grid, the area proximate their intersection will assume a net charge sufficient to attract or repel pigment particles in suspension in the dielectric fluid. Since numerous cathode and grid lines are employed, there are numerous discrete intersection points which can be controlled by varying the voltage on the cathode and grid elements to cause
25 localized visible regions of pigment concentration and rarefaction. Essentially then, the operating voltages on both cathode and grid must be able to assume at least two states corresponding to a logical one and a logical zero. Logical one for the cathode may either correspond to attraction or repulsion of

pigment. Typically, the cathode and grid voltages are selected such that only when both are a logical one at a particular intersection point, will a sufficient electrostatic field be present at the intersection relative to the anode to cause the writing of a visual bit of information on the display through migration of pigment particles. The bit may be erased, e.g., upon a reversal of polarity and a logical zero-zero state occurring at the intersection coordinated with an erase voltage gradient between anode and cathode. In this manner, digitized data can be displayed on the electrophoretic display.

Accordingly, in order to erase the image in an electrophoretic display of this type, the cathode is biased positively with respect to the anode, i.e. to create an electric field of opposite polarity. In the aforementioned type of electrophoretic display device, the anode is a unitary planar structure to which one voltage is applied in the write mode and a different voltage is applied in the erase mode. All lines of the displayed image are erased simultaneously upon application of the erase voltage anode, and all lines of the display must be rewritten to form the next image frame. The next frame may often have character lines for image portions which are the same as the previous frame, which results in the redundancy of rewriting numerous identical lines from frame to frame.

U.S. Patent Number 5,066,946, issued on November 19, 1991, to Frank J. DiSanto and Denis A. Krusos, entitled "Electrophoretic Display Panel With Selective Line Erasure", which is also owned by the assignee of the present application, discloses an electrophoretic display panel which provides for selective line erasure. The electrophoretic display apparatus of this patent has grid and cathode conductors arranged as an X-Y matrix spaced from an anode with an electrophoretic dispersion in between them. Pigment particles in the dispersion become charged at selected intersection areas of the X-Y matrix and migrate towards the anode to form a display image thereon by

biasing the cathode negatively with respect to the anode, and the display image is erased by oppositely biasing the cathode and anode. The anode is formed by a plurality of parallel line segments corresponding to image lines of the display. Each anode line segment is a longitudinal rectangular conductor having a height which corresponds to the height of a text character line. Line control means are also provided and have a plurality of switch elements for individually switching the potential applied to an anode line segment to be erased from a first potential for writing to a second, different potential for erasing the line segment, while all the other line segments that are not to be erased are maintained at the first potential.

Although single line erasure is preferable to erasure of all the lines in the display, it is not an optimally efficient manner when it is desired to change only select image characters in the display.

Accordingly, it is an object of the present invention to provide a method and an electrophoretic display which overcomes the aforementioned disadvantages of the prior art devices. In particular, the object of the invention is to provide an electrophoretic display in which individual characters of the display can be selectively erased and rewritten without disturbing the other image characters which remain the same from one frame to the next.

DISCLOSURE OF THE INVENTION

Pursuant to this object, and others which will become apparent hereafter, one aspect of the present invention resides in an electrophoretic display having a segmented anode, with each respective segment of the anode being arranged in a corresponding character position to define a character block. A plurality of parallel cathode control lines are arranged in a given direction while a plurality of parallel grid control lines are arranged perpendicular to the cathode control lines. The anode is arranged above the

cathode and grid control lines so as to have a space therebetween which accommodates an electrophoretic dispersion including pigment particles suspended in a fluid. The character blocks are interconnected by the horizontal row control lines and the vertical column control lines through a resistor in parallel with a series connected diode and resistor.

In a write mode all of the anode character blocks are provided with a positive voltage by making all of the column lines and row lines have a positive voltage. Voltages applied to the grid and cathode lines are controlled so that the pigment particles are urged toward the anode in the write mode. In order to erase a single anode character block the column line and row line corresponding to the desired character block are both provided with a negative voltage. This negative voltage from both the row and column causes the block to be erased by forcing the pigment particles toward the cathode and grid lines. Only the selected block is erased since all the remaining blocks have at least one row or column with the positive write voltage.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a cross sectional view of an electrophoretic display panel employing a character block anode structure constructed in accordance with a first embodiment of the present invention;

FIG. 2 is a partial perspective view of an electrophoretic display panel substrate provided with an array of character blocks in accordance with the embodiment of FIG. 1;

FIG. 3A is a schematic circuit representation of the electrical connections to an individual character block in accordance with the embodiment of FIG. 1;

FIG. 3B is a schematic circuit representation illustrating the control of a row of character blocks in accordance with the embodiment of FIG. 1;

5 FIG. 3C is a schematic circuit representation of the electrical connections to an individual character block in accordance with another embodiment of the present invention;

FIG. 4 is a table indicating character block voltage; and

FIG. 5 is a cross-section through the display panel of FIG. 1 pursuant to the present invention.

10

BEST MODE FOR CARRYING OUT THE INVENTION

An electrophoretic display device 10 employing the segmented anode configuration of the present invention may be seen, for example, by reference to FIG. 1. It will, however, be understood by those of ordinary skill
15 in the art that the description of EPID 10 is for illustrative purposes only and that any EPID configuration may employ the novel segmented anode and associated erase control circuitry to be herein described.

As seen in FIG. 1, the illustrative electrophoretic display device 10 includes two separated substrates 12 and 14, at least one of which is
20 transparent by way of being a material such as glass or plastic. Substrate 12, which is preferably coated with an extremely thin layer of indium tin oxide (ITO), acts as a port through which a viewer of the display may discern image information set forth thereon. The ITO layer is extremely thin, on the order of
25 such as that described in U.S. Pat. No. 4,732,830, the disclosure of which is incorporated herein by reference, a pattern of horizontal lines is etched on the surface of the ITO layer to form a plurality of rows of cathode conductors 18.

Disposed upon the group of cathode conductors 18 are a series of insulator members 20. The insulator members are formed from a photoresist layer such as a phenolic resin impregnated with photoactive material which layer is deposited over the cathode line structure. The photoresist layer is treated to selectively remove photoresist where a plurality of vertical lines are deposited upon the insulator members 20 to form a plurality of column conductors or grid electrodes 22 perpendicular to the cathode conductors 18. Each grid electrode 22, as indicated, is positioned above associated cathode lines and insulated therefrom at the areas of intersection (pixels) by the insulator members 20.

A segmented anode structure, which may for example be fabricated by etching a layer of ITO deposited on substrate 14 in accordance with a conventional etching technique, is disposed on substrate 14 and is identified generally by the reference numeral 30. An insulating structure (not shown) maintains the separated substrates and associated cathodes, grid electrodes, and anode segments, and electrophoretic solution 24 containing pigment particles 26 in a single structure. An X-Y Write Control module (not shown), in a writing phase, applies a voltage to selected cathode and grid lines which causes the pigment particles 28 to migrate away from the pixel locations corresponding to intersections of energized grid and cathode lines, thereby darkening those intersections as viewed through glass substrate 12.

The anode structure 30, which represents a novel aspect of the present invention, will now be described. With continuing reference to FIG. 1, it can be seen that anode structure 30 comprises an array of anode segments or character blocks 32, with each respective character block being exactly opposite a corresponding group of grid and cathode intersections. As will soon be apparent, the thickness of the cathode and grid conductors and the spacing between adjacent conductors, as well as the size of the character to be

displayed, determine the nominal dimensions of the character blocks.

Typically, each of the row and grid conductors will have a width on the order of 112 μm while separation between adjacent conductors is typically 15 μm .

Accordingly, if the display is to utilize characters which are 16 pixels wide and 26 pixels high, then each character block should be 2.017 mm X 3.287 mm. As shown in FIG. 1, each character block 32 is isolated from adjacent blocks by a narrow insulating region 34 that is aligned with inactive areas 35 on the cathode and grid lines. It will, therefore, be apparent that the dimensions of these inactive areas will also be a function of the spacing between adjacent cathode and grid conductors, respectively.

With continuing reference to FIG. 1, the anode structure 30 also includes a plurality of row control buses 36 (shown in FIG. 2) and column control buses 38, the former and latter extending perpendicularly relative to one another. As shown more clearly in FIG. 2, the column and row control buses are separated at their points of intersection by layers of insulating material 39. Preferably, column and row control buses 36 and 38 are disposed within insulating regions 34 so that no active area of the display area is lost.

With continuing reference to FIG. 2, there is shown an array of character blocks 32 disposed on display panel 14. As will be readily appreciated by those of ordinary skill in the art, pixels are erased from a conventional EPID display when the anode plate is at a negative potential relative to the cathode and grid electrode intersections corresponding thereto. This negative bias causes the pigment particles to migrate back toward substrate 12. Accordingly, in order to accomplish the previously noted objective of providing selective character erasure, the present invention provides means for coupling each character block to row and column control buses 36 and 38, which means are selectively operable to apply a negative

potential to character blocks corresponding to characters the user desires to erase.

As shown in FIG. 2, each character block 32 is coupled to a row control bus 36 via a first resistor 40 and a first diode 42. Similarly, each character block is coupled to a column control bus 38 via a second resistor 44 and a second diode 46. The electrophoretic solution 24 between the character blocks and cathode and grid structure, which solution typically has a very high resistance, i.e. about 1×10^{10} to $1 \times 10^{12} \Omega/\text{cm}$, provides the return path for the diode and resistor currents. Preferably, an additional resistor 48 is included in series with first diode 42 in order to reduce the power dissipated in first resistor 40 when only one of the control buses coupled to a character block is at a negative potential. For an identical purpose, resistor 50 is preferably coupled in series with second diode 46.

The PN junction diodes 42 and 46, as well as resistors 40 and 44, are preferably fabricated monolithically directly on the character blocks in accordance with conventional integrated circuit fabrication techniques. For a detailed description of such techniques as applied to the fabrication of electrophoretic displays, reference may be had to U.S. Pat. No. 4,772,820, entitled Monolithic Flat Panel Display Apparatus and issued to Frank J. DiSanto et al on Sept. 20, 1988, which patent is expressed incorporated herein by reference. The electrical connections between the control buses 36 and 38 and an individual character block 32 are shown schematically in FIG. 3A.

As shown in FIG. 3A, the resistance of the electrophoretic fluid, designated by reference numeral 52, couples the circuit to ground 54 and thereby provides a return path for the circuit. The resistance values of resistors 40 and 44 must be small in comparison to the resistance of the fluid to the cathode/grid. Accordingly, in a typical installation, each of the resistors 40 and 44 may have a resistance value of 10 M Ω compared to a resistance value of 100

M Ω or more for the fluid resistance 52. As indicated above, resistors 48 and 50 are provided to reduce the voltage across resistor 40 or resistor 44 when only one of the control buses coupled to character block 32 is at a negative potential. This resistance may be relatively small with respect to resistors 48 and 50, being, for example, on the order of 100 k Ω .

With reference now to FIG. 3B, there is shown a schematic representation illustrating the selective control of a row of character blocks 32 designated 32a through 32n coupled to a common row control bus 36 in which only one of the character blocks is selected for erasure. As can be seen, each character block in the row is coupled to a separate column control bus 38a through 38n. In order to erase the pixels associated with character block 32A, a negative potential V_{neg} supplied by a voltage generator 56 is applied via row control bus 36. Similarly, a negative potential V_{neg} is supplied by a suitable voltage generator (not shown) via column control bus 38a. As will be readily apparent to those of ordinary skill in the art, since diodes 42 and 46 are reverse biased and the resistance value of resistors 40 and 44 are low with respect to the electrophoretic fluid resistance 52, the character block 32a is at a negative potential relative to the cathode/grid electrodes. Accordingly, the pigment particles are repelled away from the character block and toward the pixels defined by the grid/cathode intersections adjacent that character block.

With continuing reference to FIG. 3B, it will be seen that other character blocks in the same row as character block 38a need not be erased during the application of a negative potential to row control bus 36 and column control bus 38.

With specific reference to character block 32b, it will be observed that if the potential V_{pos} is applied thereto via column control bus 38b, the character block 32b will be biased positive relative to the grid/cathode electrodes

associated therewith. Accordingly, the pigment particles will remain adjacent the character block and thus the character of the display associated therewith will remain written. It will therefore be apparent to those of ordinary skill in the art that so long as one of the row or column control buses coupled to a given character block apply a positive potential thereto, that character block will remain at a positive potential. Accordingly, a character of the display associated with such a character block will remain written until both the column and control bus serving that character block are biased negative relative to the cathode/grid.

With reference now to FIG. 3C, there is illustrated an alternate embodiment in which the passive diode and resistor components described above are replaced with active components, such as MOSFETs or thin-film transistors. As seen in FIG. 3C, transistor 60 is associated with the row control bus 36 while transistor 70 is associated with column control bus 38. A first electrode 62 of transistor 60 is coupled to a source of positive potential V_{pos} while a second electrode 64 of transistor 60 is coupled to a source of negative potential V_{neg} . When a small signal voltage V_s is applied via row control bus 36 to the gate 66 of transistor 60, a positive potential is applied to character block. When no signal voltage is present, the gate of transistor 60 remains closed and a negative potential is applied to the character block. The gate 76 of transistor 70, which transistor also includes a first electrode 72 and a second electrode 74, is controlled in a similar manner via column control bus 38. Resistors 68 and 78 are of the same resistance value and, in an identical manner as resistors 40 and 50 of the previous embodiment, are selected to be smaller than the fluid resistance 52. Thus, character block 32 will only operate in an erase mode and hence be at a negative potential if no signal voltage is applied to the gates of transistors 60 and 70.

Figure 4 is a table which indicates the character block voltage for a 2 by 2 array of character blocks. As seen in the first line of the table, when the voltages on both rows and both columns are positive, the character blocks have a corresponding positive voltage. Line 2 of the table shows that when
5 only the first row has a negative voltage all of the character blocks maintain their positive voltage bias. Similarly, if only column 1 is negative all the character blocks remain positive. However, when both the first column and first row are made negative the character block, i.e. 1.1, which is connect to both the first column and first row is made negative and erased. All of the
10 remaining character blocks maintain their positive charge. The same principle applies when dealing with a large array such as the 80 column by 25 row array illustrated in Figure 5.

Referring to Figure 5, there is schematically shown for illustrative purposes an eighty character by twenty-five line display panel. As will thus be
15 apparent, each line includes 80 character blocks. The character blocks 32, numbered 1,1 through 25,80 are interconnected by a plurality of column control buses 38, designated as columns 1 through 80 and a plurality of row control busses 36 designated as rows 1 through 25. The column and row control buses, as described above, are perpendicular to and insulated from each other.

20 Although the present invention has been described in detail, it should be understood that various changes, substitutions, and alterations can be made herein without departing from the spirit and scope of the invention as defined in the appended claims.

CLAIMS:

1. An electrophoretic display having a plurality of character positions, including an anode comprising a plurality of character blocks, each of said blocks being arranged in one of the character positions; a plurality of parallel row control lines arranged in a given direction; a plurality of parallel column control lines perpendicular to said row control lines and insulated therefrom, said character blocks of said anode being interconnected by said row and column control lines; a matrix of cathode and grid lines spaced from said anode; an electrophoretic dispersion of particles in a suspension medium arranged between said anode and said matrix of cathode and grid lines; circuit means for connecting each of said character blocks so that each character block is connected to one row control line and one column control line; and, means for selectively applying voltage to said row and column lines to write and erase individual character blocks.

2. An electrophoretic display as defined in Claim 1, wherein said circuit means includes for each character block a first resistor connected between the character block and a column control line and, parallel thereto, a diode connected in series with a second resistor, said circuit means further including for each character block a third resistor connected between the character block and a row control line and, parallel thereto, a further diode in series with a fourth resistor.

3. An electrophoretic display as defined in Claim 2, wherein said second resistor is arranged between said diode and said column control line, and said fourth resistor is arranged between said further diode and said row control line.

4. An electrophoretic display as defined in Claim 1, wherein each row and column control line has one end to which a voltage is applicable.

5 5. An electrophoretic display as defined in Claim 1, wherein each character block is 16 pixels wide and 26 pixels high.

6. An electrophoretic display as defined in Claim 1, wherein each of said character blocks has narrow insulating regions which isolate the
10 block from adjacent blocks, said insulating regions being aligned with said cathode and grid lines.

7. An electrophoretic display as defined in Claim 2, wherein said resistor and diodes are monolithically fabricated on said anode.

15 8. An electrophoretic display as defined in Claim 2, wherein said first and third resistor have a resistance in a range of 10-20 Meg. ohms.

9. An electrophoretic display as defined in Claim 2, wherein
20 said second and fourth resistors have a resistance value in a range of 1-10 Meg ohms.

10. An electrophoretic display as defined in Claim 1, wherein said circuit means includes for each character block, separate active means
25 connecting the block to a row control line and to a column control line.

11. An electrophoretic display as defined in Claim 10, wherein said active means includes a MOSFET connecting the character block to the

row control line and a second MOSFET connecting the character block to a column control line.

12. An electrophoretic display as defined in Claim 10, wherein
5 said active means includes a first thin-film transistor connecting the character block to a row control line and a second thin-film transistor connecting the character block to a column control line.

13. An electrophoretic display as defined in Claim 1, wherein
10 said means for selectively applying voltage to said row and column lines includes column control means, row control means, and means for controlling the column and row control means.

14. An electrophoretic display as defined in Claim 1, wherein
15 said means for selectively applying voltage to said row and control lines applies a negative voltage for erasing the character blocks.

15. A method for selectively addressing character positions on
an electrophoretic display, comprising the step of: providing an anode having a plurality of character blocks, each of said character blocks corresponding to
20 one of the character positions on the display; interconnecting the character blocks with a plurality of column and row control lines which are insulated from one another; connecting each character block to one row control line by circuit means; connecting each character block to a column control line by further circuit means; and selectively erasing a character block by applying a
25 negative voltage to the row control line and the column line that intersect at the selected character block.

16. A method as defined in Claim 15, wherein said step of applying a negative voltage includes applying a negative voltage to more than one column control line to address more than one character block on the row.

5 17. A method as defined in Claim 13, wherein said step of connecting each character block to a row control line includes connecting the character block by a first resistor and, parallel to said first resistor, a series connected diode and second resistor, said step of connecting each character block to a column control line including connecting the character block by a
10 third resistor and, parallel thereto, a series connected diode and fourth resistor.

18. A method as defined in Claim 13, wherein said steps of connecting each character block to one row control line and one column control line include connecting the character block using active electronic
15 components.

19. A method as defined in Claim 18, wherein said step of connecting the character blocks to row control lines and column control lines includes making the connection by using one of MOSFETs and thin-film
20 transistors.

20. An electrophoretic display, comprising: an anode having a plurality of character blocks; a plurality of parallel row control lines arranged in a given direction; a plurality of parallel column control lines perpendicular
25 to said row control lines and insulated therefrom, said character blocks of said anode being interconnected by said row and column control lines; a plurality of parallel cathode lines; a plurality of parallel grid lines perpendicular to said cathode lines and insulated therefrom so as to form an X-Y matrix, said matrix

of cathode and grid lines being arranged at a distance from said anode; an electrophoretic dispersion of particles in a suspension medium arranged between said anode and said matrix; means for controlling and applying voltage to selected cathode and grid lines of the X-Y matrix so as to cause the particles
5 in the suspension medium to migrate toward the anode to write in the character blocks; circuit means for connecting each of said character blocks to one row control line and one column control line; means for selectively applying voltage to said row and column lines to write and erase individual character blocks, erasing a character block said voltage applying means
10 applying a voltage to the row and column line corresponding to the character block to be erased so that the particles in the suspension medium are caused to migrate away from the anode toward the X-Y matrix; and means for integrally controlling said means for applying voltage to said X-Y matrix and said means for applying voltage to said row and column control lines.

AMENDED CLAIMS

[received by the International Bureau on 11 January 1995 (11.01.95); original claims 9, 10 and 13 cancelled; original claims 1-8, 11 and 12 amended; claims 14-20 amended and renumbered as claims 11-17 (5 pages)]

1. An electrophoretic display for displaying characters in a plurality of character positions, wherein said display includes a cathode structure, a grid structure, an anode structure and an electrophoretic suspension
5 disposed in between said cathode structure and said anode structure, an improved anode structure comprising:
 - a plurality of conductive character blocks arranged in a matrix containing columns and rows, wherein each of said blocks is separate from each other and each of said blocks corresponds in position to one of the
10 character positions in said display;
 - a single row control line coupled to each of said character blocks in a common row in said matrix;
 - a single column control line coupled to each of said character blocks in a common column in said matrix, and;
 - 15 means for selectively applying voltage to one said row control line and one said column control line to selectively change the voltage of one of said conductive character blocks in said matrix, thereby creating a reaction in said electrophoretic suspension between said anode structure and one of said conductive character blocks that enables an individual character in said
20 plurality of character positions to be selectively altered without changing other character blocks in said matrix on the same row and column.

2. An anode structure as defined in Claim 1, further including a first resistor and a first diode connecting each of said character
25 blocks to a corresponding column control line and, a second resistor and a second diode connecting each of said character blocks to a corresponding row column line.

3. An anode structure as defined in Claim 2, wherein said first resistor and said first diode are arranged in parallel and said second resistor and said second diode are arranged in parallel.

4. An anode structure as defined in Claim 3, further including a first current limiting resistor arranged in series with said first diode and a second current limiting resistor arranged in series with said second diode.

5. An anode structure as defined in Claim 1, wherein each of said character blocks is approximately 16 pixels wide and 26 pixels high.

6. An anode structure as defined in Claim 1, wherein said cathode structure includes a plurality of conductive lines and said anode structure includes a plurality of conductive lines, and said anode structure further includes a plurality of insulating regions disposed between each of said character blocks said insulating regions being aligned with the conductive lines on said cathode structure and said grid structure.

7. An anode structure as defined in Claim 4, wherein said first current limiting resistor and said first diode are monolithically formed and said second current limiting resistor and said second diode are monolithically formed on said anode structure.

8. An anode structure as defined in Claim 2, wherein said electrophoretic suspension has a predetermined resistance and said first resistor and said second resistor have a resistance less than said predetermined resistance.

9. An anode structure as defined in Claim 1, further including a first MOSFET connecting each of said character blocks to a corresponding row control line and a second MOSFET connecting each of said character blocks to a corresponding column control line.

5.

10. An anode structure as defined in Claim 1, further including a first thin-film transistor connecting each of said character blocks to a corresponding row control line and a second thin-film transistor connecting each of said character blocks to a corresponding column control line.

10

11. An anode structure as defined in Claim 1, wherein said means for selectively applying voltage to one said row line and one said control line applies a negative voltage for erasing the character blocks.

15

12. A method for selectively addressing character positions on an electrophoretic display, comprising the steps of:

providing an anode having a plurality of conductive character blocks arranged in a matrix containing columns and rows, wherein each of said character blocks are separate from each other and each of said blocks correspond to one of the character positions on the display;

20

interconnecting each of said character blocks in a row of said matrix with a row control line and interconnecting each of said character blocks in a column of said matrix with a column control line; and

selectively erasing a selected character block in said matrix by applying a negative voltage to the row control line and the column line that intersect at the selected character block.

25

13. A method as defined in Claim 12, wherein said step of applying a negative voltage includes applying a negative voltage to more than one column control line to address more than one character block.

30

14. A method as defined in Claim 12, wherein said step of interconnecting includes the substeps of:

connecting each of the character blocks to a row control line by a first resistor and, parallel to said first resistor, a series connected diode and second resistor, and

connecting each of the character blocks to a column control line by a third resistor and, parallel thereto, a series connected diode and fourth resistor.

15. A method as defined in Claim 12, wherein said steps of interconnecting includes connecting each of the character blocks to a corresponding row control line and a corresponding column control line using active electronic components.

16. A method as defined in Claim 15, wherein the character blocks are connected to row control lines and column control lines by thin-film transistors.

17. An electrophoretic display, comprising:
an anode having a plurality of separate character blocks arranged in a matrix having columns and rows;

a plurality of parallel row control lines arranged in between said rows of said matrix, wherein each of said block characters in each of said lines in said matrix is coupled to a single common row control line;

a plurality of parallel column control lines perpendicular to said row control lines and arranged between said columns of said matrix, wherein each of said block characters in each of said columns in said matrix is coupled to a single common column control line;

a plurality of parallel cathode lines;

a plurality of parallel grid lines perpendicular to said cathode lines and insulated therefrom so as to form an X-Y matrix, said X-Y matrix of cathode and grid lines being arranged at a distance from said anode;

an electrophoretic dispersion of particles in a suspension medium
5 arranged between said anode and said X-Y matrix;

means for controlling and applying voltage to selected cathode and grid lines of the X-Y matrix so as to cause the particles in the suspension medium to migrate toward the anode to write in the character blocks;

means for selectively applying voltage to said row and column lines to
10 write and erase individual character blocks, erasing a character block said voltage applying means applying a voltage to the row and column line corresponding to the character block to be erased so that the particles in the suspension medium are caused to migrate away from the anode toward the X-Y matrix; and

15 means for integrally controlling said means for applying voltage to said X-Y matrix and said means for applying voltage to said row and column control lines.

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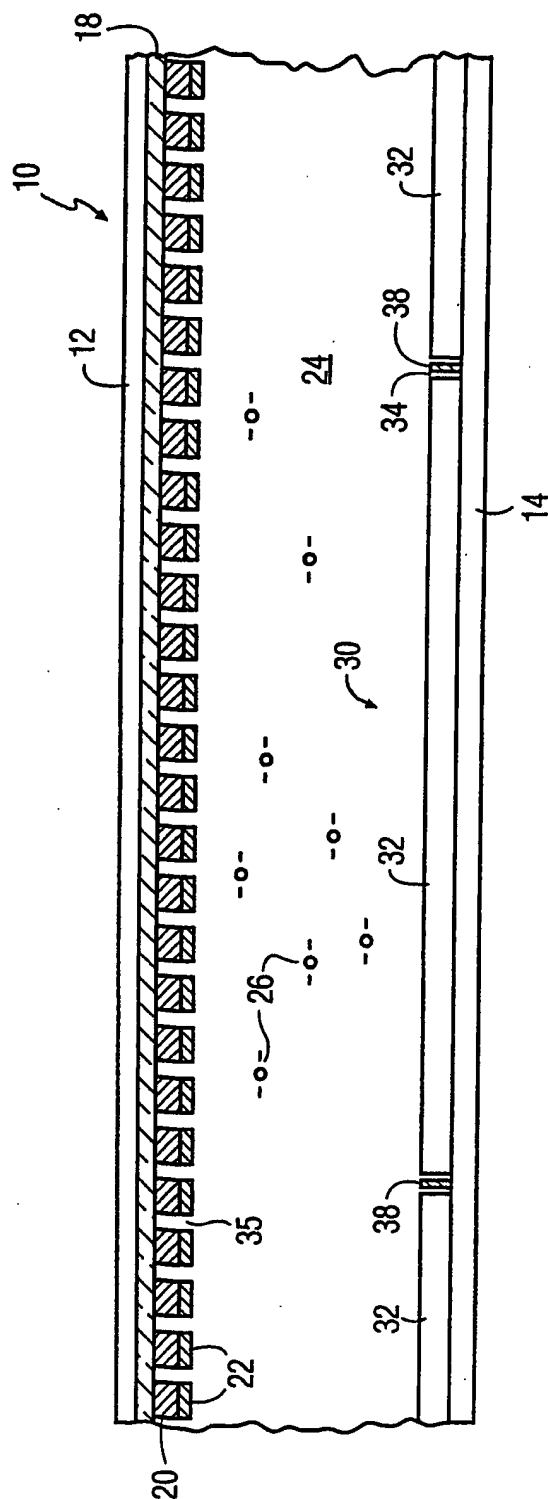


FIG. 1

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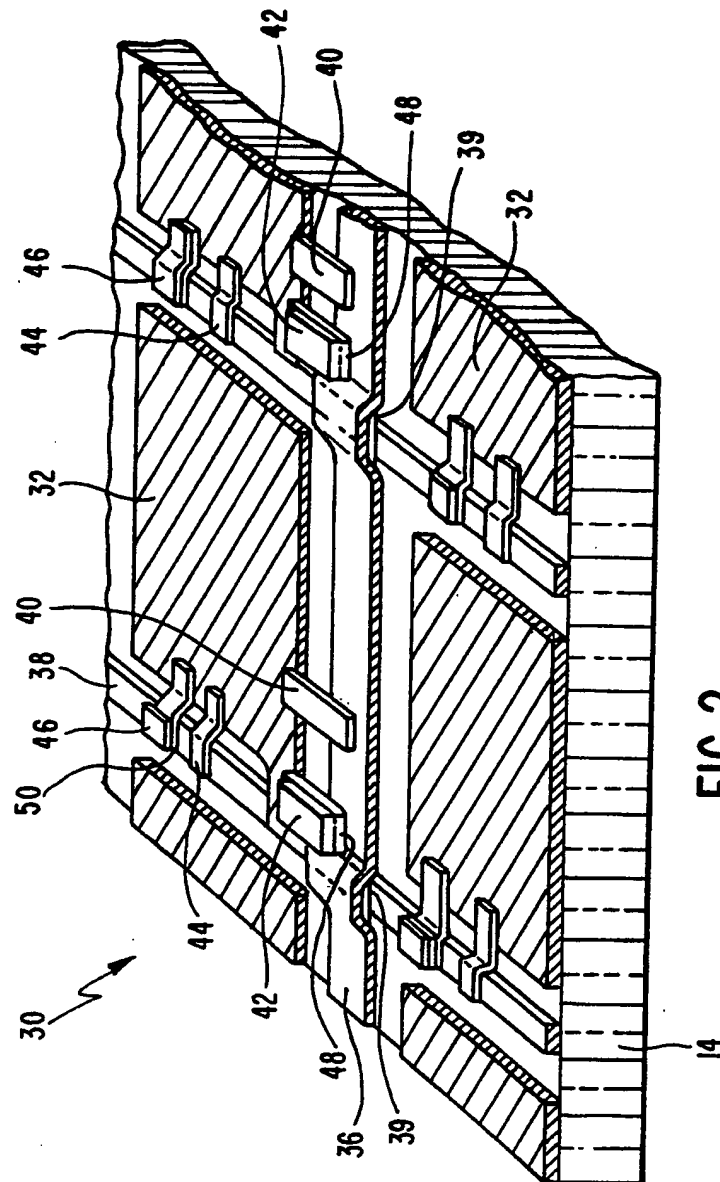


FIG. 2

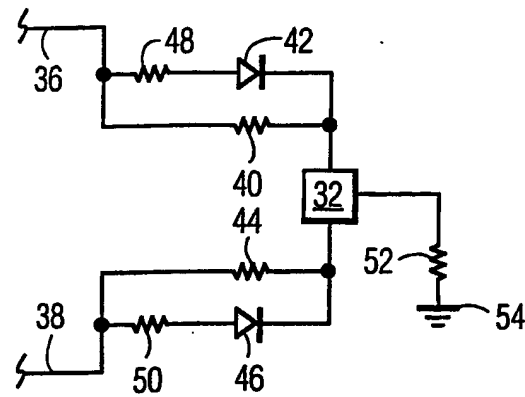


FIG. 3A

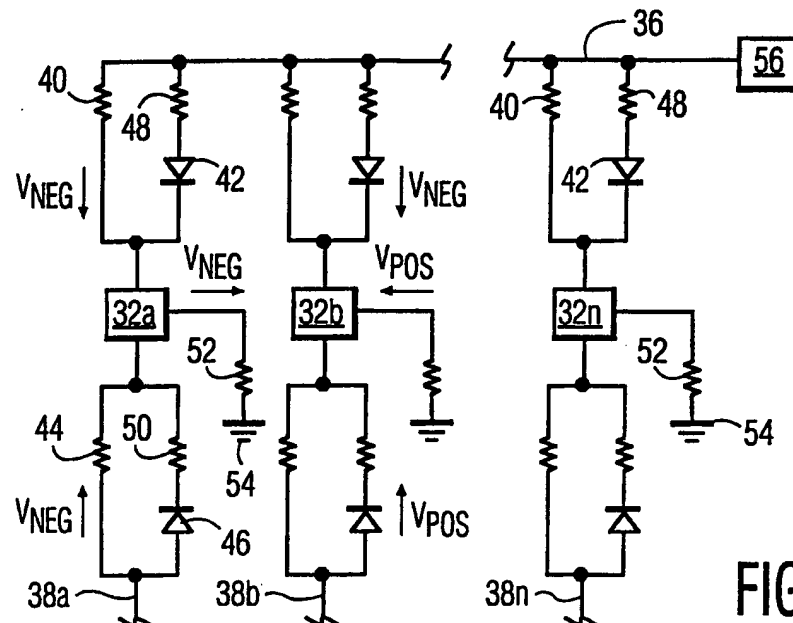


FIG. 3B

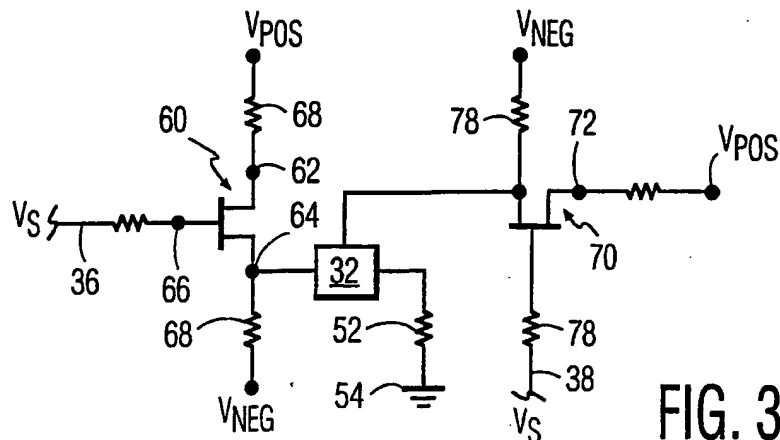


FIG. 3C

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V COL. 2	V ROW 2	V COL. 1	V ROW 1	V BLOCK (1,1)	V BLOCK (1,2)	V BLOCK (2,1)	V BLOCK (2,2)
+V	+V	+V	+V	+V	+V	+V	+V
+V	+V	+V	-V	+V	+V	+V	+V
+V	+V	-V	+V	+V	+V	+V	+V
+V	+V	-V	-V	-V	+V	+V	+V

FIG. 4

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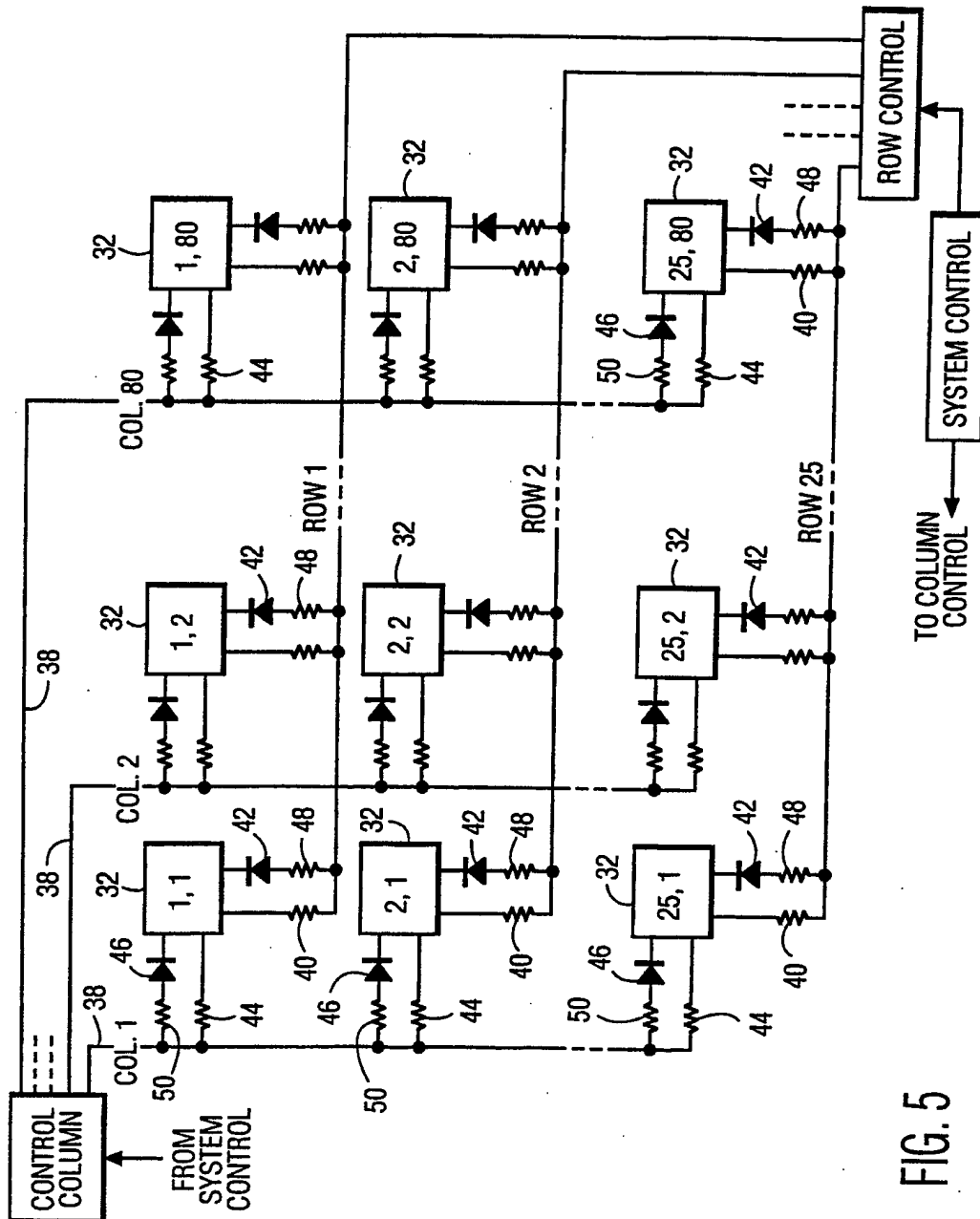


FIG. 5

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US94/09208

A. CLASSIFICATION OF SUBJECT MATTER

IPC(5) :G09G 3/34.

US CL :345/107.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 345/107; 359/294,295,296,297.

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X ----- Y	US, A, 5,066,946 (DISANTO et al) 19 November 1991, col. 2, lines 10-13, col. 5, lines 8-42.	1-6, 8, 9, 14-16, 20 ----- 7, 10-13, 17-19
Y	US, A, 4,772,820 (DISANTO et al) 20 September 1988, col. 3, lines 3-15.	7
Y	US, A, 4,448,491 (OKUBO) 15 May 1984, col. 1, lines 49-51.	10-12, 18, 19
Y	US, A, 4,522,472 (LIEBERT et al) 11 June 1985, Figure 2.	13, 17

☐ Further documents are listed in the continuation of Box C.
 ☐ See patent family annex.

* Special categories of cited documents:	*T	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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L document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	*G*	document member of the same patent family
O document referring to an oral disclosure, use, exhibition or other means		
P document published prior to the international filing date but later than the priority date claimed		

Date of the actual completion of the international search

31 OCTOBER 1994

Date of mailing of the international search report

09 DEC 1994

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 Box PCT
 Washington, D.C. 20231

Facsimile No. (703) 305-3230

 Authorized officer
Regina Liang
 REGINA LIANG

Telephone No. (703) 305-4700